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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventors: Jifeng LI

Application No.: 10/563,559

Filed: January 5, 2006

For: DECODING APPARATUS AND DECODING METHOD

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
Washington, DC 20231

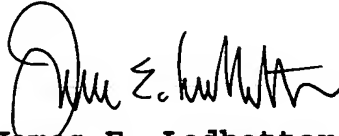
Dear Sir:

Pursuant to Rules 56 and 99, Applicants hereby call the attention of the Patent Office to the documents listed on the attached Form PTO 1449. All of these references were cited in a Supplementary European Search Report dated June 16, 2006 (copy attached).

Applicants present this art so that the Patent Office may, in the first instance, determine any relevancy thereof to the presently claimed invention, see Beckman Instruments, Inc. v. Chemtronics, Inc., 439 F.2d 1369, 1380, 165 USPQ 355, 364 (5th Cir. 1970). Also see Patent Office Rules 104 and 106. Applicants respectfully request that this art be expressly considered during the prosecution of this application and made of record herein and

appear among the "References Cited" on any patent to issue herefrom.

Respectfully submitted,



James E. Ledbetter
Registration No. 28,732

Date: August 28, 2006

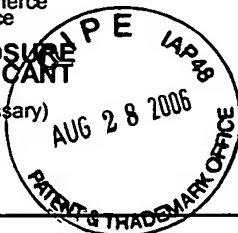
JEL/ejw

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FORM PTO-1449 U.S. Department of Commerce
(Rev. 4/92) Patent and Trademark Office

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)



ATTY. DOCKET NO.

L9289.05199

SERIAL NO.

10/563,559

APPLICANT

Jifeng LI

FILING DATE

January 5, 2006

GROUP

Unassigned

U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

Supplementary European Search Report dated June 16, 2006.

M. El-Assal, et al., "A High Speed Architecture for Map Decoder," IEEE, XP010616579, Oct. 16, 2002, pp. 69-74.

M. Bickerstaff, et al., "A 24Mb/s Radix-4 LogMAP Turbo Decoder for 3GPP-HSDPA Mobile Wireless," Regular Paper ISSCC, XP010661378, Feb. 9, 2003, pp. 1-10.

Z. Wang, et al., Area-Efficient High-Speed Decoding Schemes for Turbo Decoders," IEEE Transactions on Very Large Scale Integration (VLSI) Systems, XP-002335788, Vol. 10, No. 6, Dec. 2002, pp. 902-912.

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449 [6-4])